

CP 2714  
PATENT APPLICATION

IN THE U.S. PATENT AND TRADEMARK OFFICE

August 24, 2000

Applicant(s) : Toru AIDA et al  
Title : CONTOUR EMPHASIZING CIRCUIT  
Serial No. : 09/463 642 Group : 2714  
Filed : January 24, 2000 Examiner: Unknown

International Application No.: PCT/JP98/03317

International Filing Date : July 24, 1998

Atty. Docket No.: Furusawa C-49

Box Non-Fee Amendment

Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

Herewith is an amendment in the above-identified application.

☐ Statement(s) re small entity status submitted previously.☐ Statement(s) re small entity status enclosed.☒ No additional filing fee is required.☐ The additional filing fee has been calculated as shown below:

For	No. Filed	No. Extra	(X) LG Entity	RATE	( ) SM Entity	Fee
Basic Fee				\$690.00	\$345.00	\$0.00
Total Claims	(11 - 20 = 0)		x \$	18.00	x \$	9.00
Indep. Claims	( 2 - 3 = 0)		x \$	78.00	x \$	39.00
<input type="checkbox"/> Multiple Dep. Claim			+	\$260.00	+	\$130.00
* * * TOTAL FILING FEE * * *						\$ 0.00

☐ A Check for \$ is enclosed to cover fees.

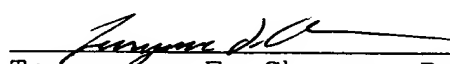
☒ Please credit any overpayment, or charge any additional filing fee or application processing fee required under 37 CFR 1.16 or 1.17 by this communication, to Deposit Account No. 06-1382. A duplicate copy of this sheet is enclosed.

IN DUPLICATE

Respectfully submitted,

TFC/R/km

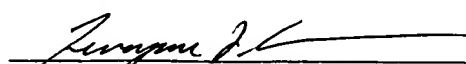
Encl: Listed above

  
Terryence F. Chapman Reg. No. 32 549-----  
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on August 24, 2000.

Respectfully submitted,

130.9912

  
Terryence F. Chapman

#5  
Add C  
9/6/00  
MDH

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SECOND AMENDMENT BEFORE FIRST OFFICE ACTION

Sir:

Prior to issuance of the first Office Action in the  
above-identified application, kindly enter the following:

IN THE CLAIMS

Please add Claims 10-15 as follows:

cl  
10. A contour emphasizing circuit comprising a contour pick-up unit for sampling a contour component from an input luminance signal, a decoder for decoding a luminance level corresponding to the luminance level of a given input luminance signal from among the luminance level divided into n number of levels from predetermined upper bits of the input luminance signal, a coefficient control unit comprising multipliers and gates for selectively changing n number of coefficients according to the decoded signal from the decoder as a gate control signal, that is, selecting a high-value contour emphasizing coefficient for a high luminance level and a low-value contour emphasizing coefficient for a low luminance level to multiply the contour component sampled by means of the contour pick-up unit for outputting the product thereof, and an adder for adding the contour component output